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**RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2661**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Govind MALALUR et al.

Art Unit: 2661

Application No.: 09/841,189

Examiner: Brian D. Nguyen

Filed: April 25, 2001

Attorney Dkt. No.: 58268.00000

For: GIGABIT SWITCH ON CHIP ARCHITECTURE

**RESPONSE UNDER 37 CFR § 1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 19, 2005

Sir:

In response to the Office Action dated September 28, 2005, please amend the above-identified application as set forth below.

**Amendments to the claims are submitted on page 2.**

**Remarks are submitted on page 12.**